

**Notice of References Cited**

Application/Control No.

09/045,118

Applicant(s)/Patent Under  
Examination  
SUZUKI ET AL.

Examiner

Neal Berezny

Art Unit

2823

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**U.S. PATENT DOCUMENTS**

*		DOCUMENT NO.	DATE	NAME	CLASS	SUBCLASS	DOCUMENT SOURCE **	
							APS	OTHER
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**FOREIGN PATENT DOCUMENTS**

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**NON-PATENT DOCUMENTS**

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<input type="checkbox"/>	U	Wolf, S.; Silicon Processing for the VLSI Era; Vol. 2; Lattice Press; Sunset Beach, Ca.; 1990; pp.194-198.	<input type="checkbox"/>	<input checked="" type="checkbox"/>
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